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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Terry L. Goode
Assignee: IKOS Systems, Inc.
Title: User Logic IO Signals On The Programmable Logic Device (PLD)
Serial No.: 09/841,974 Filing Date: April 24, 2001
Examiner: Unassigned Group Art Unit: Unassigned
Docket No.: M-11050 US

San Jose, California
May 14, 2001

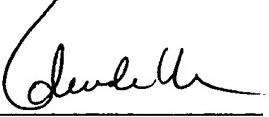
Attn: Official Draftsperson
COMMISSIONER FOR PATENTS
Washington, D. C. 20231

SUBMISSION OF FORMAL DRAWINGS

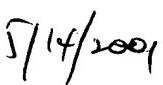
Dear Sir:

Applicant submits two (2) sheets of formal drawings, consisting of Figures 1 and 2, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (408) 453-9200.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on May 14, 2001.

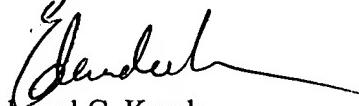


Attorney for Applicant



Date of Signature

Respectfully submitted,


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